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PATENT

Case Docket No.: 00P7882US

Date: September 13, 2000

J.B.
11-02-01

ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231
Box PATENT APPLICATION

Sir:
Transmitted herewith for filing is the patent
application of:

Inventor(s): Hartmund TERLETZKI and Gerd FRANKOWSKY

For: Level-Shifting Circuitry Having "High" Output
Impedance During Disable Mode

This application includes:

10 / pages: specification, claims and abstract
1 sheets of drawings
— photographs

Also enclosed is:

— Declaration and Power of Attorney
— Information Disclosure Statement pursuant to 37 CFR 1.56.

The filing fee has been calculated as shown below:

FOR:	(Col. 1) NO. FILED	(Col. 2) NO. EXTRA RATE	OTHER THAN A SMALL ENTITY FEE
BASIC FEE	XXXXXXXX	XXXXXXXXXX	XXXXX \$690
TOTAL CLAIMS	6 - 20 =		x 18. = \$
INDEP CLAIMS	1 - 3 =		x 78. = \$
* MULTIPLE DEPENDENT CLAIM PRESENTED			+260. = \$
			TOTAL \$690.

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